Xyloni Development Kit Overview

The Xyloni Development kit features the 易灵思® T8 programmable device in a 81-ball FBGA package. T8 FPGAs have a programmable logic and routing fabric built on 易灵思® Quantum™ technology. T8 devices also include embedded memory blocks, multiplier blocks (or DSP blocks), a PLL, and an oscillator. The Xyloni development board features:

• 易灵思® T8F81C2 device in an 81-ball FineLine BGA package

• 128 Mbit SPI NOR flash memory

• Dedicated UART interface through micro-USB type B

• 41-pin high-speed connector for user I/O

• 12-pin PMOD-compatible GPIO connector

• Micro-SD card slot

• 4 user LEDs and 2 user pushbutton switches

• 33.33 MHz oscillator

• User selectable voltages from 1.8 V, 2.5 V, and 3.3 V

TRION® FPGA

Makers and hobbyists can explore Trion FPGAs with the thumb-sized Xyloni development board
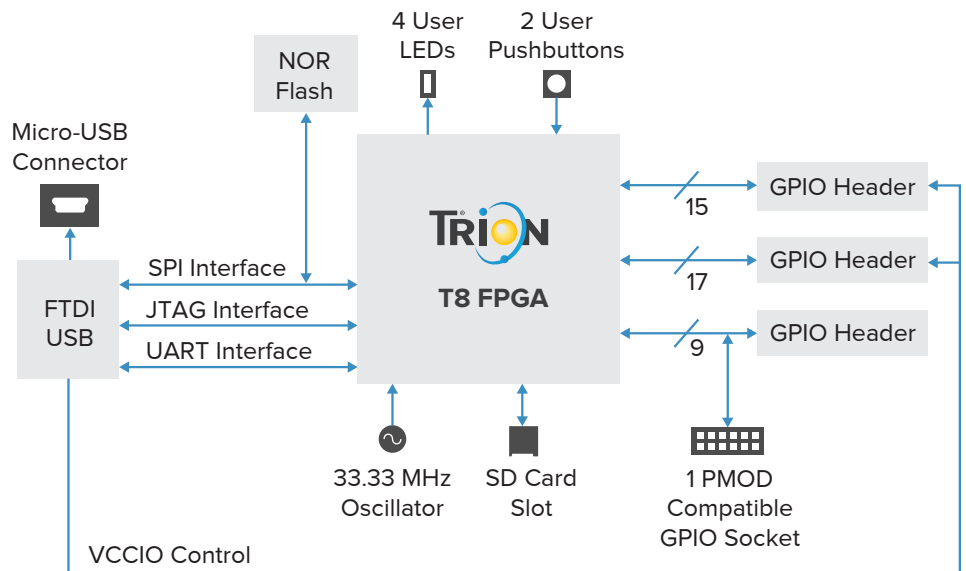

Figure 1   Xyloni Development Board Block Diagram

Figure 2   Xyloni Development Kit

Efinity Software Support

You can create designs for the Trion development kit using the 易灵思 Efinity© software. The Efinity software provides a complete tool flow from RTL design to bitstream generation, including synthesis, place-and-route, and timing analysis. The software has a graphical user interface (GUI) that provides a visual way to set up projects, run the tool flow, and view results. The software also has a commandline flow and Tcl command console. The software-generated bitstream file configures Trion devices. The software supports the Verilog HDL and VHDL languages.

Availability

The Xyloni Development Kit is available now. Visit us online at www.elitestek.com for a listing of distributors and sales representatives.